



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Sapna George et al.
 Application No. : 09/486,582
 Filed : July 10, 2000
 For : FAST SYNTHESIS SUB-BAND FILTERING METHOD FOR
 DIGITAL SIGNAL DECODING

Examiner : Andrew C. Flanders
 Art Unit : 2615
 Docket No. : 851663.407
 Date : May 8, 2007

Mail Stop AF
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

05/11/2007 CNEGA1 00000046 09486582
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents:

In accordance with 37 CFR 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached Supplemental Information Disclosure Statement. Copies of cited U.S. patents and published patent applications are not required and accordingly have not been provided. Copies of any other cited references are enclosed. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 CFR 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

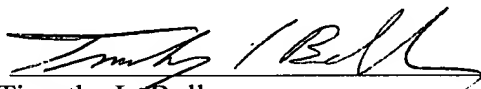
Applicant wishes to disclose and have officially considered the following co-pending application by the same assignee: Application No. 09/622,736, filed October 27, 2000,

entitled, "FAST FREQUENCY TRANSFORMATION TECHNIQUE FOR TRANSFORM AUDIO CODERS."

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

A fee of \$180 is submitted in accordance with 37 CFR 1.97. The Director is authorized to charge any other fees which may be required, or credit any overpayment to Deposit Account No. 19-1090.

Respectfully submitted,
Seed Intellectual Property Law Group PLLC



Timothy L. Boller
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TLB:jms

Enclosures:

Check

Supplemental Information Disclosure Statement

Cited References (5)

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Sheet 1 of 1U.S. DEPARTMENT OF COMMERCE
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ATTY. DOCKET NO.

851663.407

APPLICATION NO.

09/486,582

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANTS

Sapna George et al.

FILING DATE

July 10, 2000

GROUP ART UNIT

2615

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,181,183	01/19/93	Miyazaki	364	725	
	AB	5,479,562	12/26/95	Fielder et al.	395	2.38	
	AC	5,592,584	01/07/97	Ferreira et al.	395	2.12	
	AD	5,727,119	03/10/98	Davidson et al.	395	2.12	
	AE	5,805,484	09/08/98	Ikeda et al.	364	726.01	
	AF	5,864,800	01/26/99	Imai et al.	704	229	
	AG	5,991,787	11/23/99	Abel et al.	708	400	
	AH	6,304,847	10/16/01	Jhung	704	500	
	AI	6,587,590	07/01/03	Pan	382	250	
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK	0 590 790 A2	06/04/94	EP		
	AL	0 718 746 A1	06/26/96	EP (+ English abstract)		X
	AM	0 564 089 B1	01/20/99	EP		
	AN	0 506 111 B1	04/12/00	EP		
	AO					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AP	Proakis et al., "Digital Signal Processing: Principles, Algorithms, and Applications," Third Edition, 1996, pp. 290-292, 415, and 475-477.
	AQ	
	AR	

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).